

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

SCANNED

**Q.A.**

APPLICATION NO. 09/909658	CONT/PRIOR	CLASS 324 714	SUBCLASS 727	ART UNIT <del>2558</del> 2133	EXAMINER Abraham
<p>APPLICANTS Chandrasekhar Srinivasalah  Udupi Harisharan.  Chidambaram Ramaswamy</p> <p>TITLE Boundary scan cell for testing AC coupled line using phase  modulation technique in si</p>					

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div> <div>_____ (Assistant Examiner) _____ (Date)</div> <div>_____ (Primary Examiner) _____ (Date)</div> <div>_____ (Legal Instruments Examiner) _____ (Date)</div> </div>		<b>NOTICE OF ALLOWANCE MAILED</b> <div>_____</div>	
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